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FIG.1A

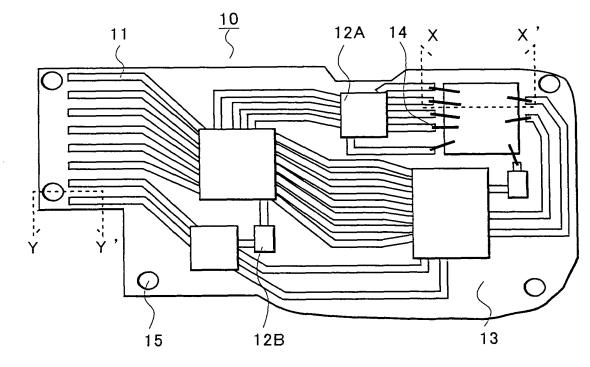


FIG.1B

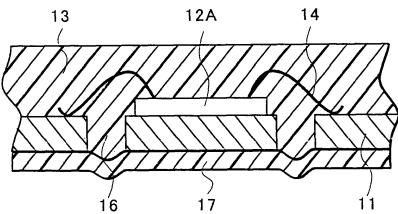
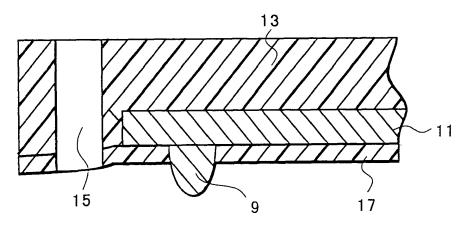


FIG.1C



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FIG.2

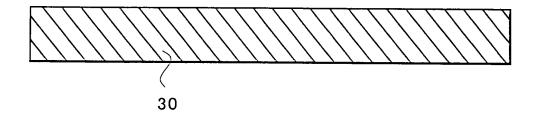
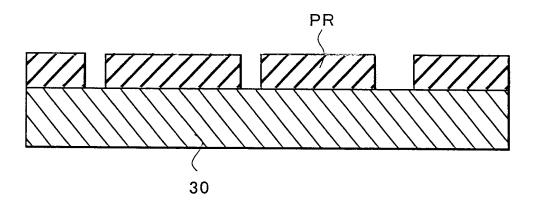


FIG.3



SAME

FIG.4A

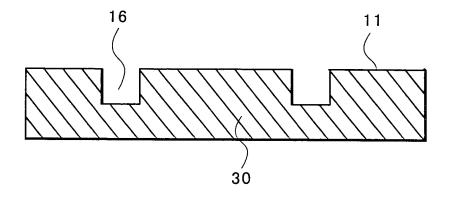
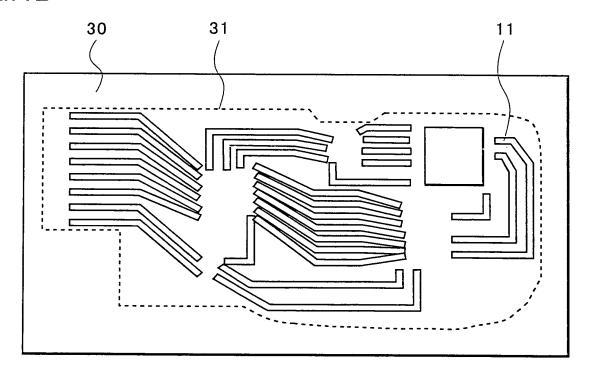


FIG.4B



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FIG.5A

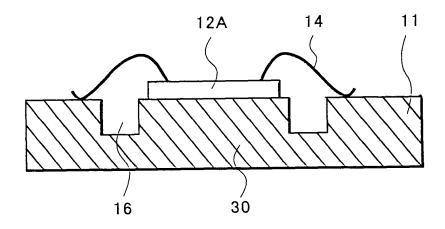
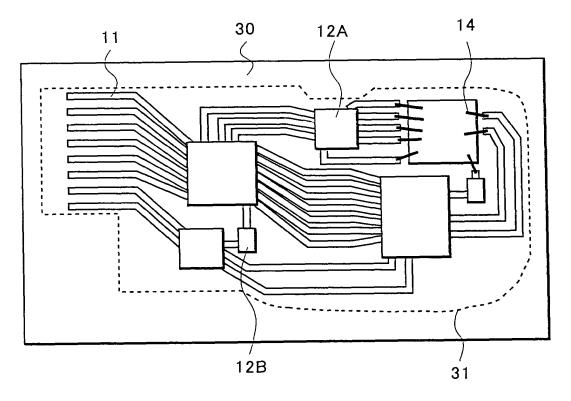


FIG.5B



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FIG.6A

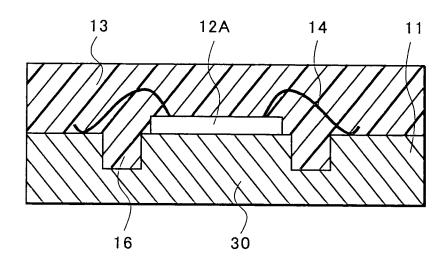
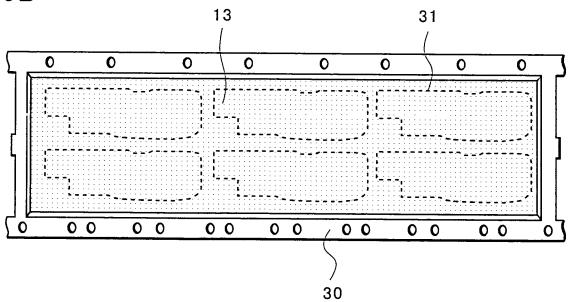


FIG.6B



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FIG.7

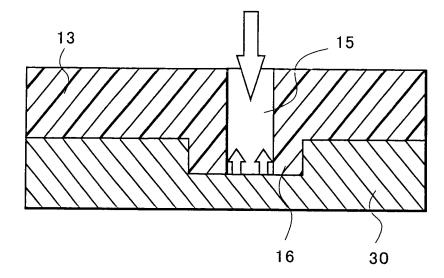


FIG.8

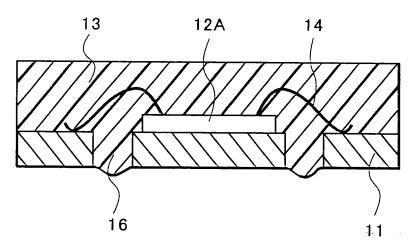
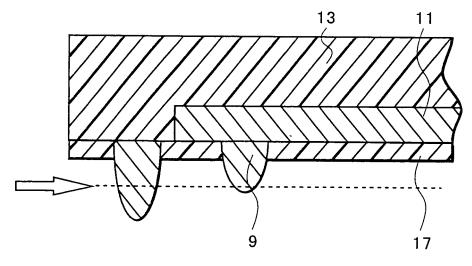


FIG.9



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FIG.10A

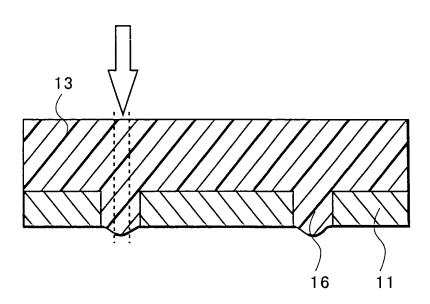
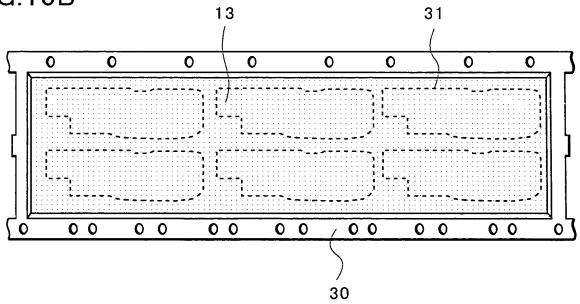


FIG.10B



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FIG.11A 8/18

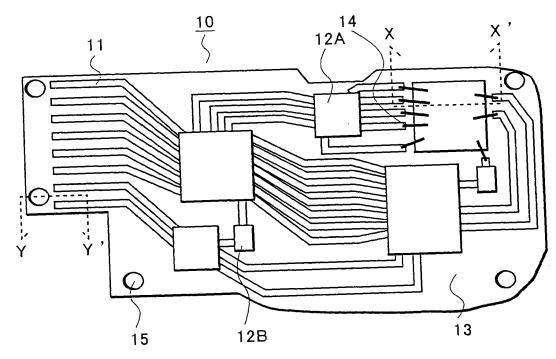


FIG.11B

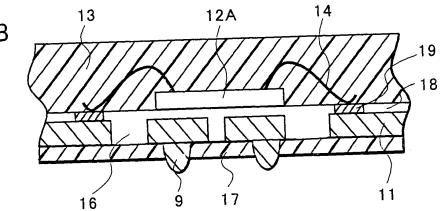
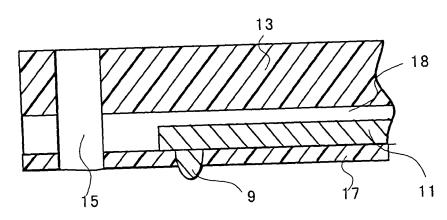


FIG.11C



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SAME

FIG.12

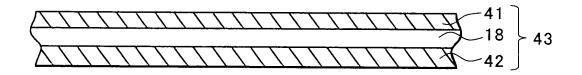


FIG.13

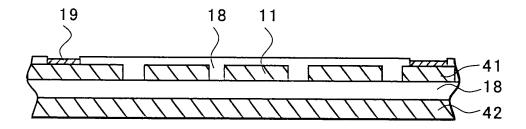
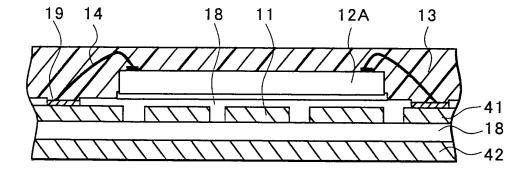


FIG.14



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FIG.15

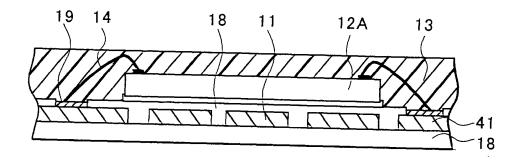
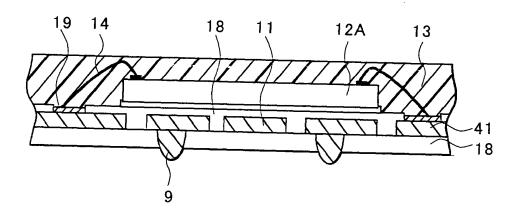


FIG.16



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FIG.17

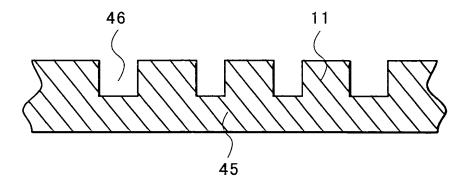


FIG.18

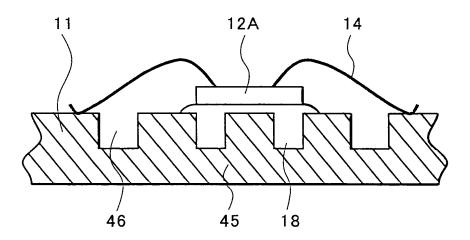
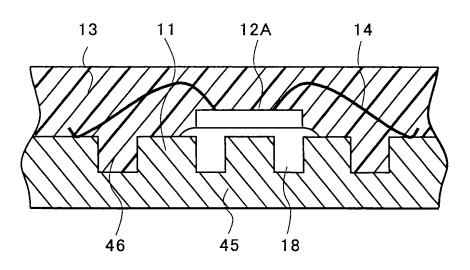


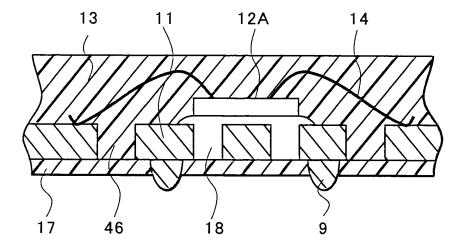
FIG.19



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## FIG.20



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SAME

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FIG.21A

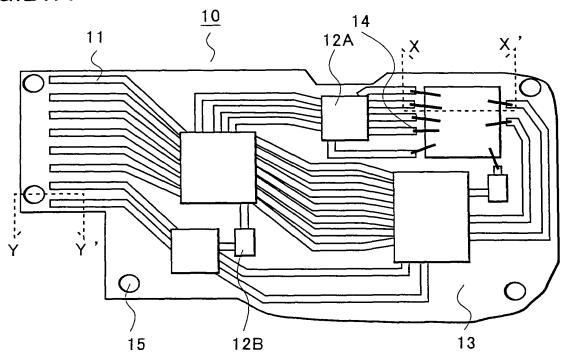


FIG.21B

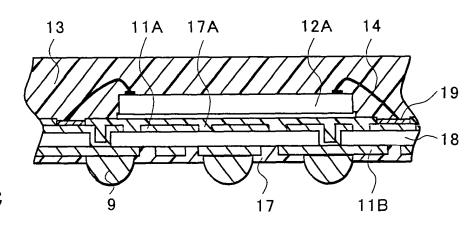
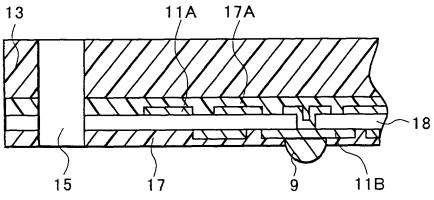


FIG.21C



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CIRCUIT DEVICE AND METHOD OF MANUFACTURING THE
SAME

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FIG.22

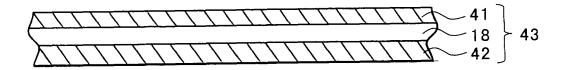


FIG.23

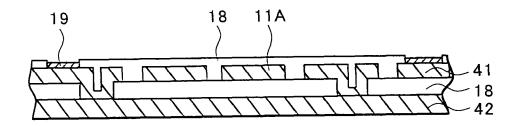


FIG.24

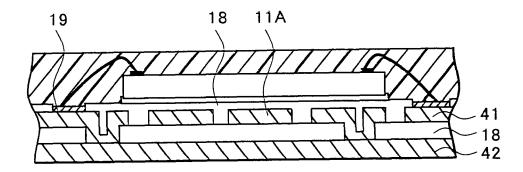


FIG.25

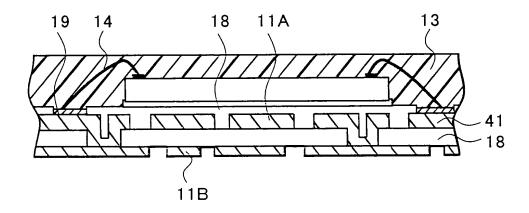
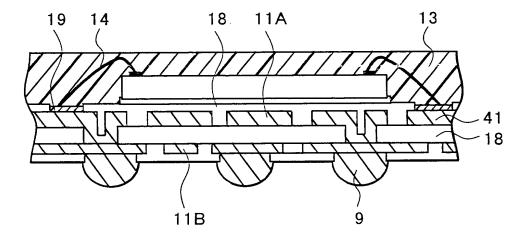
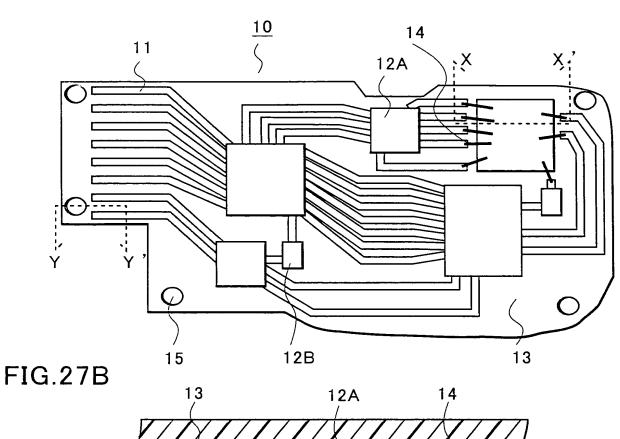


FIG.26



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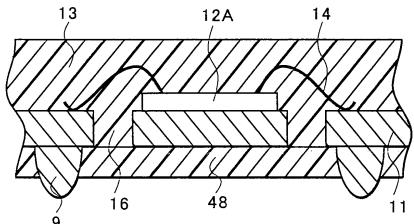
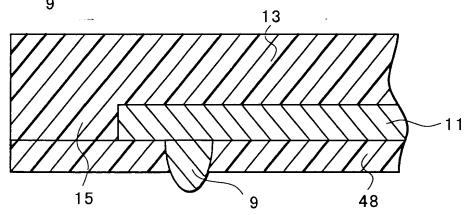


FIG.27C



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FIG.28

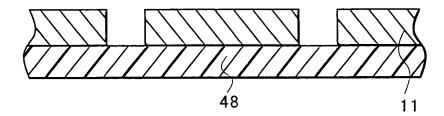


FIG.29

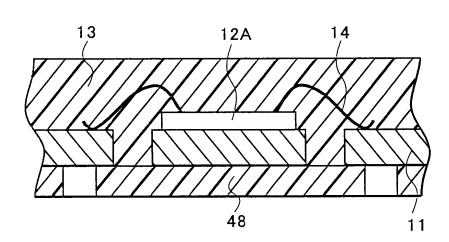
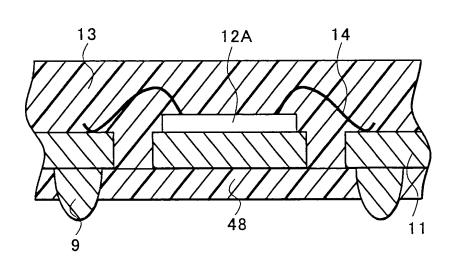


FIG.30



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FIG.31

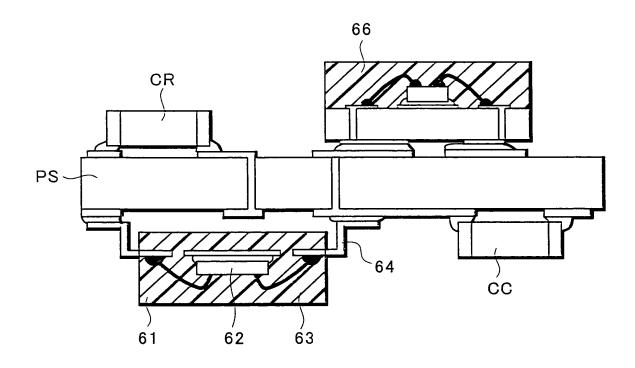


FIG.32

